



**Sterne Kessler
Goldstein Fox**
ATTORNEYS AT LAW

Robert Greene Sterne
Jorge A. Goldstein
David C.S. Cornwall
Robert W. Esmond
Tracy-Gene G. Durkin
Michelle A. Gimbalia
Michael B. Ray
Robert E. Sokohl
Eric K. Stoffe
Michael Q. Lee
John M. Covert
Robert C. Millonig
Donald J. Featherstone
Timothy J. Shea, Jr.
Michael V. Messinger
Judith U. Kim
Jeffrey T. Helvey
Eldora L. Ellison
Donald R. Barowitz

Peter A. Jackman
Brian J. Del Buono
Mark Fox Evers
Vincent L. Capuano
Elizabeth J. Haanes
Michael D. Specht
Kevin W. McCabe
Glenn J. Perry
Theodore A. Wood
Edward W. Yee
Grant E. Reed
Jason D. Eisenberg
Tracy I. Muller
Jon E. Wright
LuAnne M. DeSantis
Helene C. Carlson
Cynthia M. Bourchier
Timothy A. Doyle
Gabby L. Longworth

Lori A. Gordon
Laura A. Vogel
Shannon A. Carroll
Anbar F. Khal
Michelle K. Holoubek
Marsha A. Rose
Scott A. Schaller
Lei Zhou
W. Blake Coblenz
James J. Pohl
John T. Haran
Mark W. Rygiel
Michael R. Naalek
Carla Ji-Eun Kim
Doyle A. Siever
Ulrike Winkler-Jenks
Paul A. Calvo
C. Matthew Rozier
Randall K. Baldwin

Daniel J. Nevirv
Lori M. Brandes
Jeffrey K. Mill*
Mita Mukherjee*
Scott M. Woodhouse*
Christian A. Camarce*
Richard D. Collier III*
Keisha Hyton-Rodic
Bonnie Hanninga-Combs
Alyssa K. Sandrowitz
Jonathan M. Strang*
Ishan P. Weerakoon*

Registered Patent Agents*
Karen R. Markowicz
Matthew J. Dowd
Peter A. Socarras
Danielle L. Letting
Steven C. Oppenheimer

Aaron S. Lukas
Gaurav Ansharia
Stephanie L. Elmer
Robert C. Bakin
Salvador M. Bezos
Yasser Mountada

Of Counsel
Edward J. Kessler
Kenneth C. Bass III
Marvin C. Guthrie
Christopher P. Wrist
David C. Isaacson

*Admitted only in Maryland
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*Practice Limited to
Federal Agencies

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Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Re: U.S. Patent No. 6,746,826
Issued: July 25, 2000
For: **Method for an Improved Developing Process in Wafer
Photolithography**
Patentees: HEON *et al.*
Our Ref: 1857.6850000

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Power of Attorney to Prosecute Applications Before the USPTO; and
2. Statement Under 37 C.F.R. 3.73(b).

The above-listed documents are filed electronically through EFS-Web.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Glenn J. Perry
Attorney for Patentees
Registration No. 28,458

GJP/awt
Enclosures

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